

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

ECH8653 — General-Purpose Switching Device Applications

Features

- · Low ON-resistance
- · Best suited for LiB charging and discharging switch
- · Halogen free compliance

- 4V drive
- · Common-drain type
- · Protection diode in

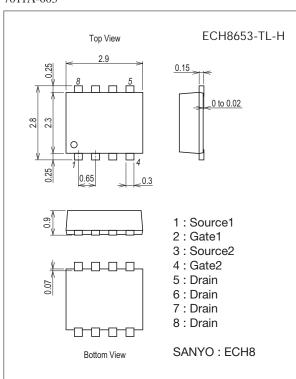
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		7.5	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	40	А
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm ² x0.8mm) 1unit	1.4	W
Total Power Dissipation	PT	When mounted on ceramic substrate (900mm ² ×0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7011A-003



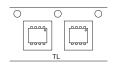
Product & Package Information

• Package : ECH8

• JEITA, JEDEC :-

• Minimum Packing Quantity : 3,000 pcs./reel

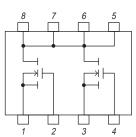
Packing Type: TL



Marking



Electrical Connection

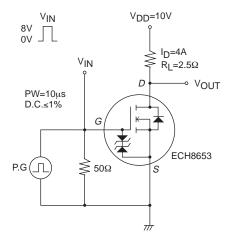


ECH8653

Electrical Characteristics at Ta=25°C

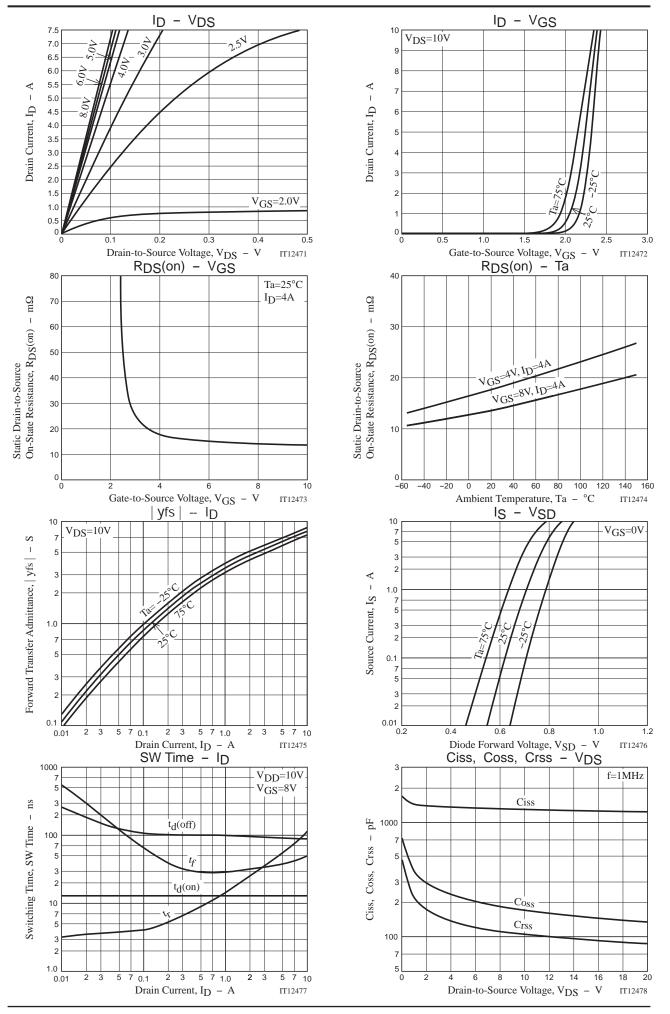
Parameter	Cumbal	Conditions	Ratings			Unit	
Parameter	Symbol	Conditions	min	typ	max	Unit	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	20			V	
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ	
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	1.0		2.4	V	
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =4A	3.4	5.8		S	
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =4A, V _G S=8V	9	14	20	mΩ	
	R _{DS} (on)2	I _D =4A, V _G S=4V	11	18	25	mΩ	
Input Capacitance	Ciss			1280		pF	
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		170		pF	
Reverse Transfer Capacitance	Crss			105		pF	
Turn-ON Delay Time	t _d (on)			13		ns	
Rise Time	t _r	San appointed Toot Circuit		48		ns	
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		94		ns	
Fall Time	tf			36		ns	
Total Gate Charge	Qg			18.5		nC	
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =8V, I _D =7.5A		2.7		nC	
Gate-to-Drain "Miller" Charge	Qgd			3.1		nC	
Diode Forward Voltage	VSD	IS=7.5A, VGS=0V		0.82	1.2	V	

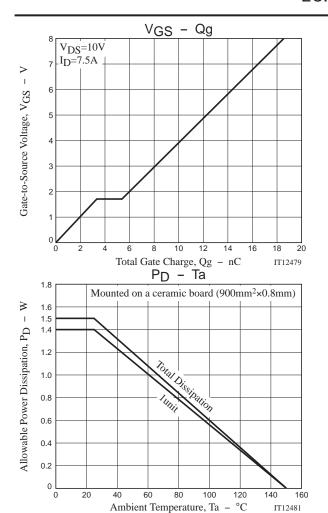
Switching Time Test Circuit

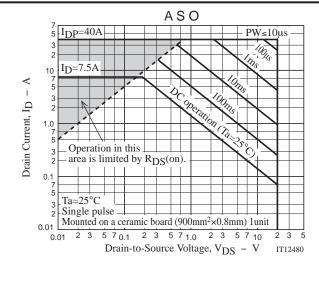


Ordering Information

Device	Package	Shipping	memo	
ECH8653-TL-H	ECH8653-TL-H ECH8		Pb Free and Halogen Free	





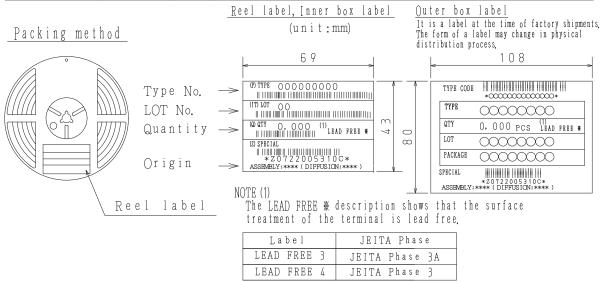


Embossed Taping Specification

ECH8653-TL-H

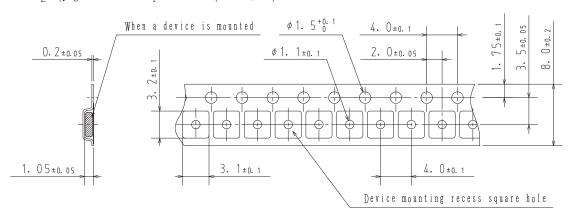
1. Packing Format

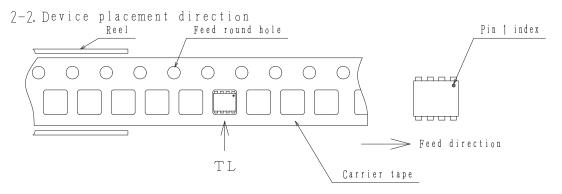
Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing	format
	Туре	Reel	Inner box	Outer box	Inner $BOX(C-1)$	Outer BOX (A-7)
ECH8	СРН6	3, 000	15, 000	90,000	5 reels contained	6 inner boxes contained
					Dimensions:mm (external)	Dimensions:mm (external)
					183×72×185	440×195×210



2. Taping configuration

2-1. Carrier tape size (unit:mm)

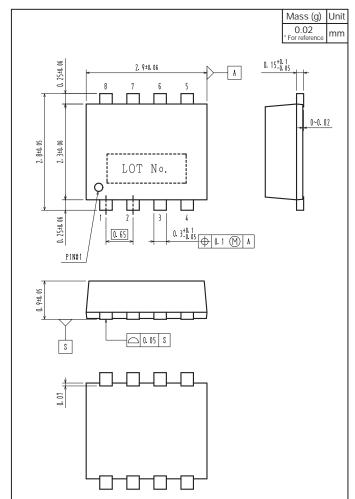




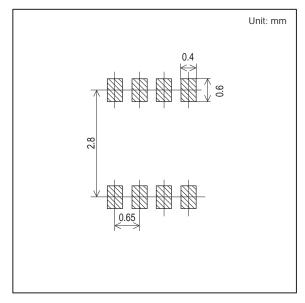
Those with pin 1 index on the feed hole side · · · · · TL

Outline Drawing

ECH8653-TL-H



Land Pattern Example



Note on usage: Since the ECH8653 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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